



CERT. No. QAC0946535
(ISO9001)

CERT. No. HKG002005
(ISO14001)

Product Specification

Customer: _____

Model Name: **H023LQ 40E2501**

Date: _____

Version: _____

Preliminary Specification

Final Specification

For Customer's Acceptance

Approved by	Comment

Approved by	Reviewed by	Prepared by

Contents

1. Record of Revision.....	3
2 .General Specifications.....	4
3 .Input/Output Terminals	5
4. Absolute Maximum Ratings	6
5 .Electrical Characteristics	6
6 .Interface Timing.....	7
7. Optical Characteristics	11
8 . Environmental / Reliability Tests	14
9. Mechanical Drawing.....	15
10.Packing.....	15
11. Precautions For Use of LCD modules	16



1. Record of Revision

Version	Revise Date	Content	Editor
1.0	2016/06/12	First Release.	Rich Liang



2 General Specifications

	Feature	Spec
Characteristics	LCD Size	2.31inch
	Display Format	320 (RGB) × 240
	Interface	6-bit RGB
	Color Depth	262K
	Technology type	a-Si
	Display Spec.	TBD
	Display Mode	Normally White
	Driver IC	ILI9342C
	Surface Treatment	Haze 20%
	Viewing Direction	12 O'clock
	Gray Viewing Direction	6 O'clock
Mechanical	LCM (W x H x D) (mm)	47.55*55.2*2.8
	Active Area(mm)	35.06 x 46.75
	With /Without TSP	Without TSP
	Weight (g)	TBD
	LED Numbers	2 LEDs

Note 1: Viewing direction is following the data which measured by optics equipment.

Note 2: Requirements on Environmental Protection: RoHS

Note 3: LCM weight tolerance: +/- 5%



3 Input/Output Terminals

No.	Symbol	Description
1,2	NC	-
3	VGL	Connect this pad with a stabilizing capacitor.
4	C4P	Pins to connect capacitance for power circuitry
5	C4M	Pins to connect capacitance for power circuitry
6	VGH	Connect this pad with a stabilizing capacitor.
7	NC	-
8	VCAC	Connect this pad with a stabilizing capacitor.
9	VINT3	Connect this pad with a stabilizing capacitor..
10	C3P	Pins to connect capacitance for power circuitry
11	C3M	Pins to connect capacitance for power circuitry
12	VINT2	Connect this pad with a stabilizing capacitor.
13	C2P	Pins to connect capacitance for power circuitry
14	C2M	Pins to connect capacitance for power circuitry
15	NC	-
16	C1P	Pins to connect capacitance for power circuitry
17	C1M	Pins to connect capacitance for power circuitry
18	PGND	Power ground
19	PVDD	Connect this pad with a stabilizing capacitor.
20	NC	-
21	LEDA	LED backlight anode.
22	NC	-
23	LEDK	LED backlight K
24	NC	-
25	GND	Power ground
26	VCC	Power supply for interface logic circuits (1.65 ~ 3.3V)
27	CS	Chip select input pin (“Low” enable).
28	SDA	Serial data input/output and applied on the rising edge of the SCL signal.
29	SCL	This pin is used serial interface clock.
30	HSYNC	Line synchronizing signal for RGB interface operation.
31	VSYNC	Frame synchronizing signal for RGB interface operation.
32	DCLK	Data enable signal in DPI interface.
33~38	D5~D0	DATA BUS
39	NC	
40	NC	



4 Absolute Maximum Ratings

Item	Symbol	MIN	MAX	Unit	Remark
Supply Voltage	V_{DD}	-0.3	5.0	V	
Input Signal Voltage	V_{in}	-0.3	$V_{DD}+0.3$	V	
Logic Output Voltage	V_{OUT}	-0.3	$V_{DD}+0.3$	V	
Operating Temperature	T_{OPR}	-10	60	°C	
Storage Temperature	T_{STG}	-20	70	°C	

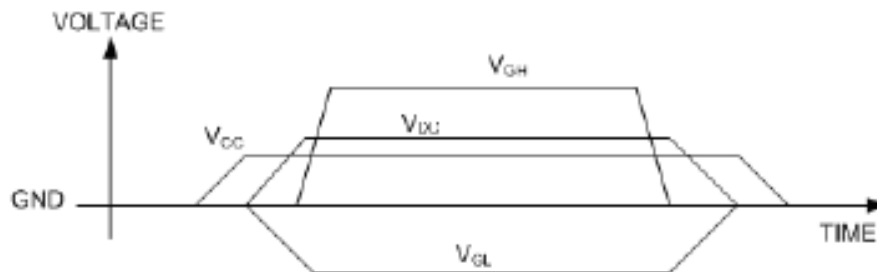
5 Electrical Characteristics

5.1 Operating conditions:

Parameter	Symbol	MIN	TYP	MAX	Unit	Remark
Power Voltage	V_{cc}	2.6	2.8	3.2	V	
Digital Operation Current	I_{cc}		8.6		mA	
Gate On Power	V_{COMH}	2.5	13.9	4.5	V	
Gate Off Power	V_{GH}	-	-13.6	15	V	
Vcom High Voltage	V_{comH}	-	3.9	-	V	Note1
Vcom low Voltage	V_{comL}	-	-1.2	-	V	Note1
Vcom level max	V_{comA}	-	-	6	V	

Note1. V_{comH} & V_{comL} : Adjust the color with gamma data. V_{p-p} should be higher than 4V. (Option 5V)

Note: Please power on following the sequence V_{CC} V_{DD}

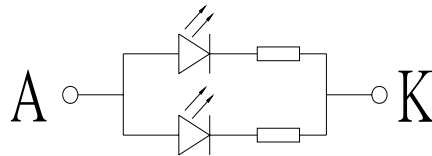




5.2 Driving Backlight

Item	Symbol	MIN	TYP	MAX	Unit	Remark
LED current	I_F	-	30		mA	Note 1 Note 2,3
Power Consumption					mW	
LED Voltage	V_F		3.2		V	
LED Life Time	W_{BL}	-	TBD	-	Hr	

Note 1 : There are 1 Groups LED



Note 2 : $T_a = 25^\circ\text{C}$

Note 3 : Brightness to be decreased to 50% of the initial value



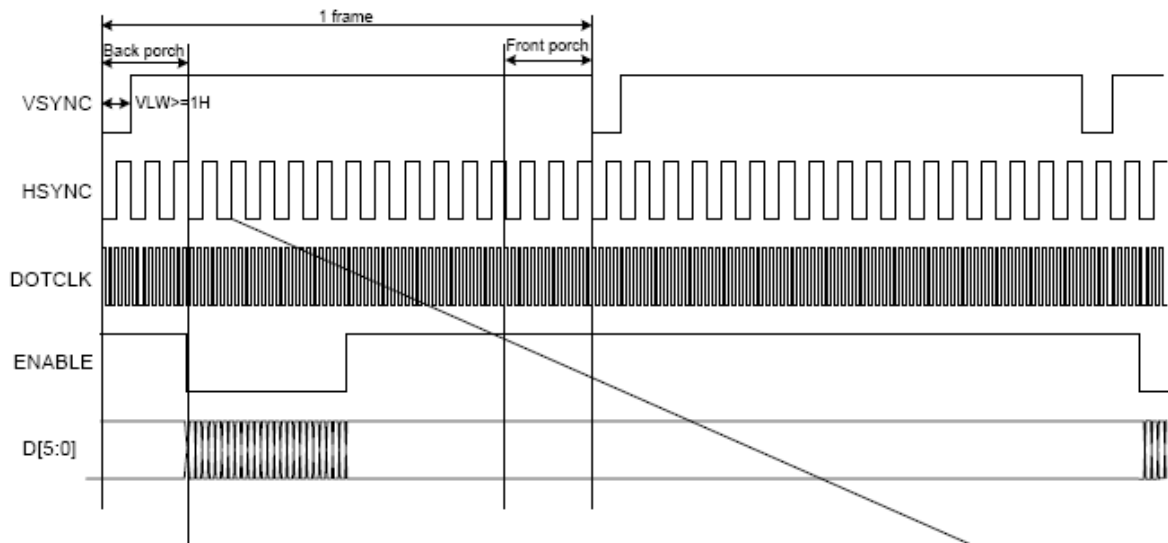
6 Interface Timing

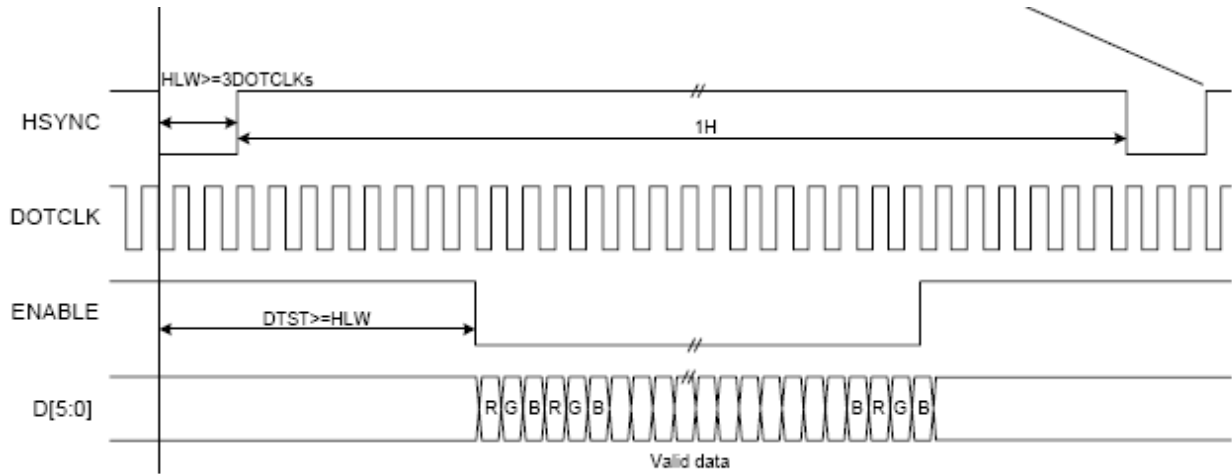
6.1 AC Electrical Characteristics

- DC Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
Input high voltage	V_{IH}	-	$0.7IOVCC$	-	$IOVCC$	V
Input low voltage	V_{IL}	-	0	-	$0.3IOVCC$	V
Voltage for logic	VCC	$T_a=25^{\circ}C$	2.5	2.8	3.3	V
Voltage for analog	VCI	$T_a=25^{\circ}C$	2.5	2.8	3.3	V
Voltage for I/O	IOVCC	$T_a=25^{\circ}C$	1.65	1.8/2.8	3.3	V
Output high voltage	V_{OH}		$0.8IOVCC$		$IOVCC$	V
Output low voltage	V_{OL}		0		$0.2IOVCC$	V
Current consumption for LCD normal operation	I_{DD}	$V_{DD} = 2.8$	-	TBD		mA

The timing chart of 6-bit RGB interface mode is shown as below:





VLW : VSYNC Low Width
HLW : HSYNC Low Width
DTST : Data Transfer Startup Time

Note 1: The DE signal is not needed when RGB interface SYNC mode is selected.

Note 2: VSPL='0', HSPL='0', DPL='0' and EPL='1' of "Interface Mode Control (B0h)" command.

Note 3: In 6-bit RGB interface mode, each dot of one pixel (R, G and B) is transferred in synchronization with DOTCLK.

Note 4: In 6-bit RGB interface mode, set the cycles of VSYNC, HSYNC and DE to 3 multiples of DOTCLK.

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7 Optical Characteristics

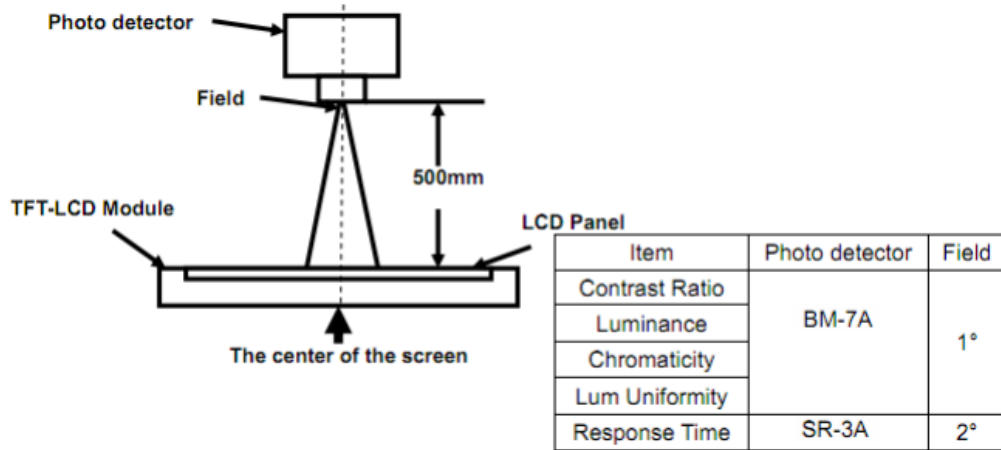
Items	Symbol	Condition	Min.	Typ.	Max.	Unit	Remark	
Viewing angles	θ_T	Center CR \geq 10		55	-	Degree.	Note2	
	θ_B			65	-			
	θ_L			65	-			
	θ_R			65	-			
Contrast Ratio	CR	$\Theta = 0$	-	350	-	-	Note1, Note3	
Response Time	T_{ON}	25°C	-	20	35	ms	Note1, Note4	
	T_{OFF}		-	20	30			
Chromaticity	White	X_W	Backlight is on	0.282	0.312	0.342	-	Note1, Note5
		Y_W		0.319	0.349	0.379	-	
	Red	X_R		0.609	0.639	0.669	-	
		Y_R		0.314	0.344	0.374	-	
	Green	X_G		0.264	0.294	0.324	-	
		Y_G		0.557	0.587	0.617	-	
	Blue	X_B		0.102	0.132	0.162	-	
		Y_B		0.106	0.136	0.166	-	
Uniformity	U		80	-	-	%	Note1, Note6	
NTSC				50		%	Note5	
Luminance	L		200	250			Note1, Note7	

Test Conditions:

1. IF= 20mA(one channel),the ambient temperature is 25°C.
2. The test systems refer to Note 1 and Note 2.

Note 1: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 5 minutes operation, the optical Properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.



Note 2: Definition of viewing angle range and measurement system.

Viewing angle is measured at the center point of the LCD by CONOSCOPE (ergo-80).

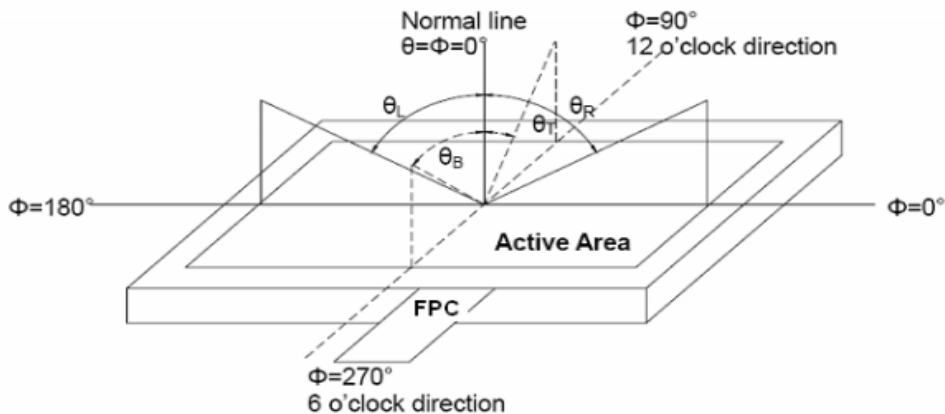


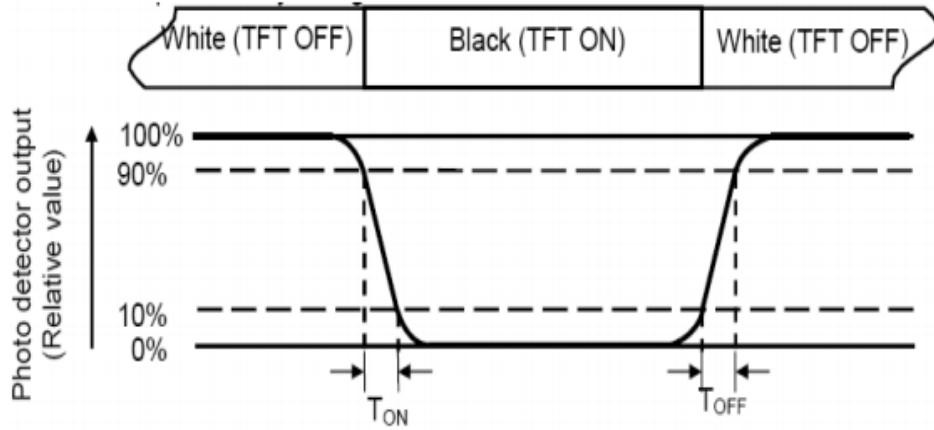
Fig. 1 Definition of viewing angle

Note 3: Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD is on the "White" state}}{\text{Luminance measured when LCD is on the "Black" state}}$$

Note 4: Definition of Response time

The response time is defined as the LCD optical switching time interval Between "White" state and "Black" state. Rise time (TON) is the time between Photo detector output intensity changed from 90% to 10%. And fall time (TOFF) is The time between photo detector output intensity changed from 10% to 90%



Note 5: Definition of color chromaticity (CIE1931)
Color coordinates measured at center point of LCD.

Note 6: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the Center of each measuring area

$$\text{Luminance Uniformity (U)} = \text{Lmin} / \text{Lmax} \times 100\%$$

L-----Active area length W----- Active area width

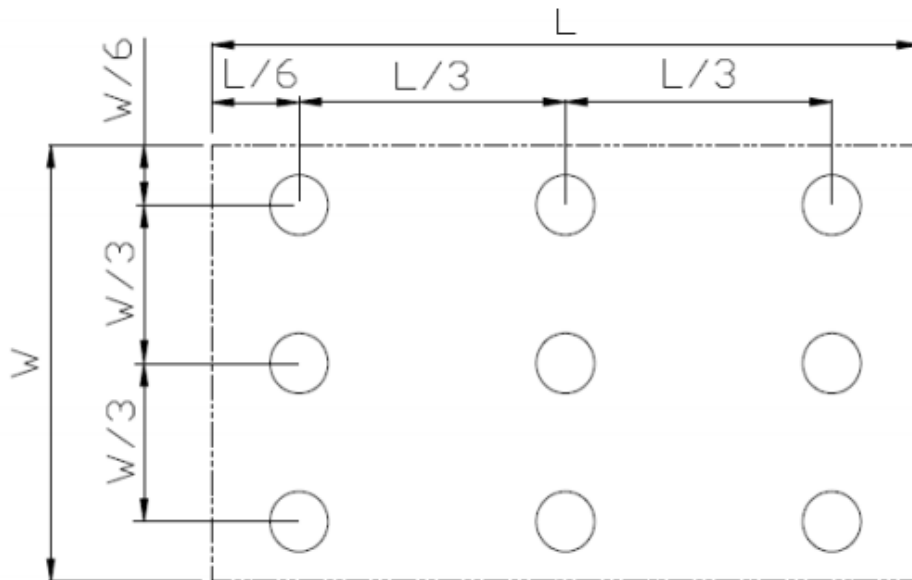


Fig. 2 Definition of uniformity

Lmax: The measured maximum luminance of all measurement position.

Lmin: The measured minimum luminance of all measurement position.

Note 7: Definition of Luminance:

Measure the luminance of white state at center point.



8 Environmental / Reliability Tests

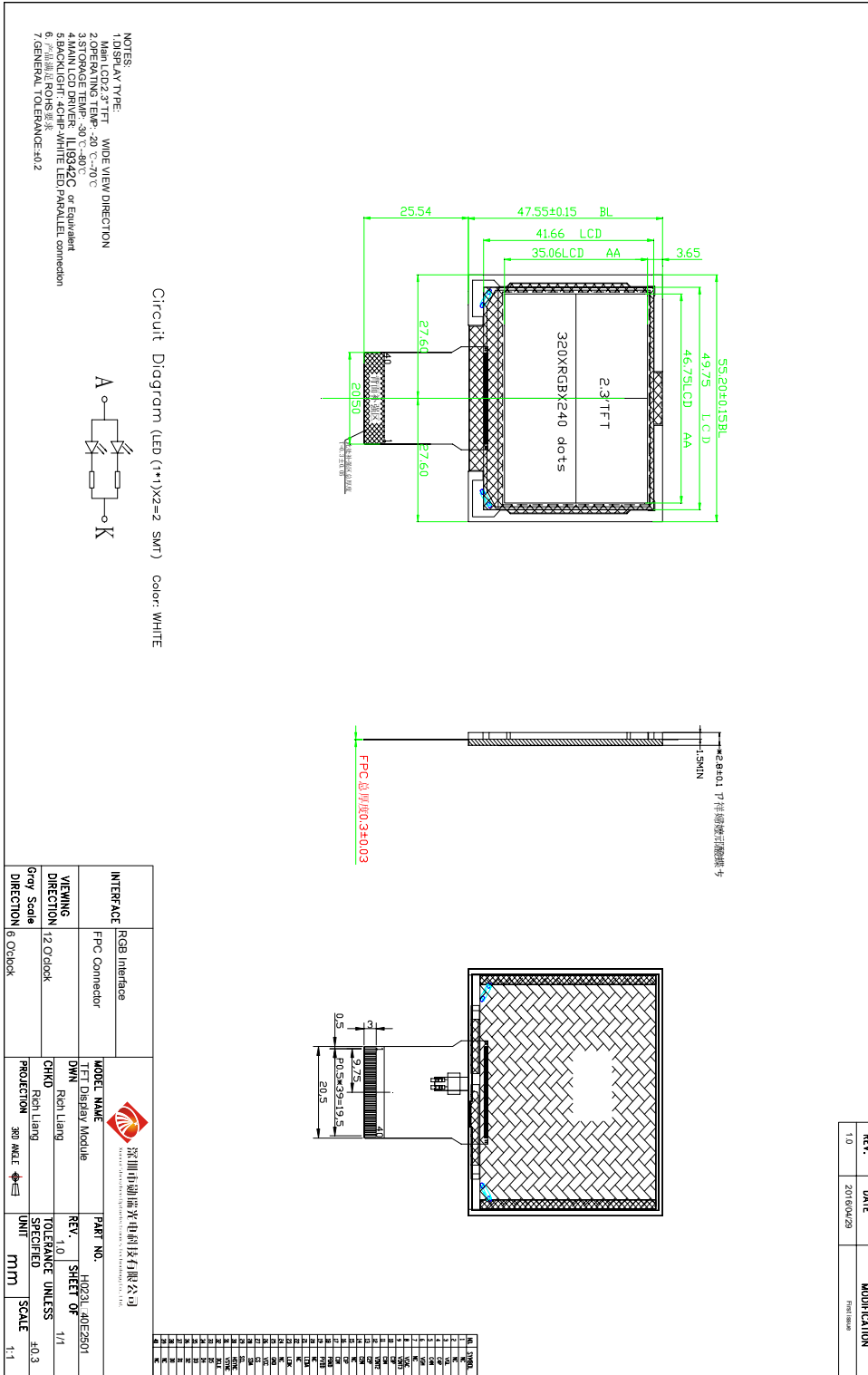
No	Test Item	Condition	Remarks
1	High Temperature Operation	T _s = +60°C, 240hrs	Note 1 IEC60068-2-2, GB2423. 2-89
2	Low Temperature Operation	T _a = -10°C, 240hrs	Note 2 IEC60068-2-1 GB2423.1-89
3	High Temperature Storage	T _a = +70°C, 240hrs	IEC60068-2-2 GB2423. 2-89
4	Low Temperature Storage	T _a = -20°C, 240hrs	IEC60068-2-1 GB/T2423.1-89
5	High Temperature & Humidity Storage	T _a = +60°C, 90% RH max, 160 hours	IEC60068-2-3 GB/T2423.3-2006
6	Thermal Shock (Non-operation)	-30°C 30 min ~ +80°C 30 min Change time: 5min, 30 Cycle	Start with cold temperature, end with high temperature IEC60068-2-14, GB2423.22-87
7	Electro Static Discharge (Operation)	C=150pF, R=330 Ω, 5 points/panel Air:±8KV, 5 times; Contact: ±4KV, 5 times; (Environment: 15°C ~ 35°C, 30% ~ 60%, 86Kpa ~ 106Kpa)	IEC61000-4-2 GB/T17626.2-1998
8	Vibration (Non-operation)	Frequency range: 10~55Hz, Stroke: 1.mm Sweep: 10Hz~55Hz~10Hz 2 hours for each direction of X .Y. Z. (package condition)	IEC60068-2-6 GB/T2423.5-1995
9	Shock (Non-operation)	60G 6ms, ± X, ±Y , ± Z 3 times for each direction	IEC60068-2-27 GB/T2423.5-1995
10	Package Drop Test	Height: 60 cm, 1 corner, 3 edges, 6 surfaces	IEC60068-2-32 GB/T2423.8-1995

Note: 1. T_s is the temperature of panel's surface.

2. T_a is the ambient temperature of sample.



9 Mechanical Drawing





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1 0.Packing

TBD



11. Precautions for Use of LCD modules

11.1 Handling Precautions

11.1.1. The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

11.1.2. If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

11.1.3. Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

11.1.4. The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

11.1.5. If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

- Isopropyl alcohol
- Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

Water ; Ketene ; Aromatic solvents

11.1.6. Do not attempt to disassemble the LCD Module.

11.1.7. If the logic circuit power is off, do not apply the input signals.

11.1.8. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

11.1.8.1. Be sure to ground the body when handling the LCD Modules.

11.1.8.2. Tools required for assembly, such as soldering irons, must be properly ground.

11.1.8.3. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

11.1.8.4. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

11.2 Storage Precautions

11.2.1. When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

11.2.2. The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : 0℃ ~ 40℃ Relatively humidity: ≤80%

11.2.3. The LCD modules should be stored in the room without acid, alkali and harmful gas.

11.3 Transportation Precautions

The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.